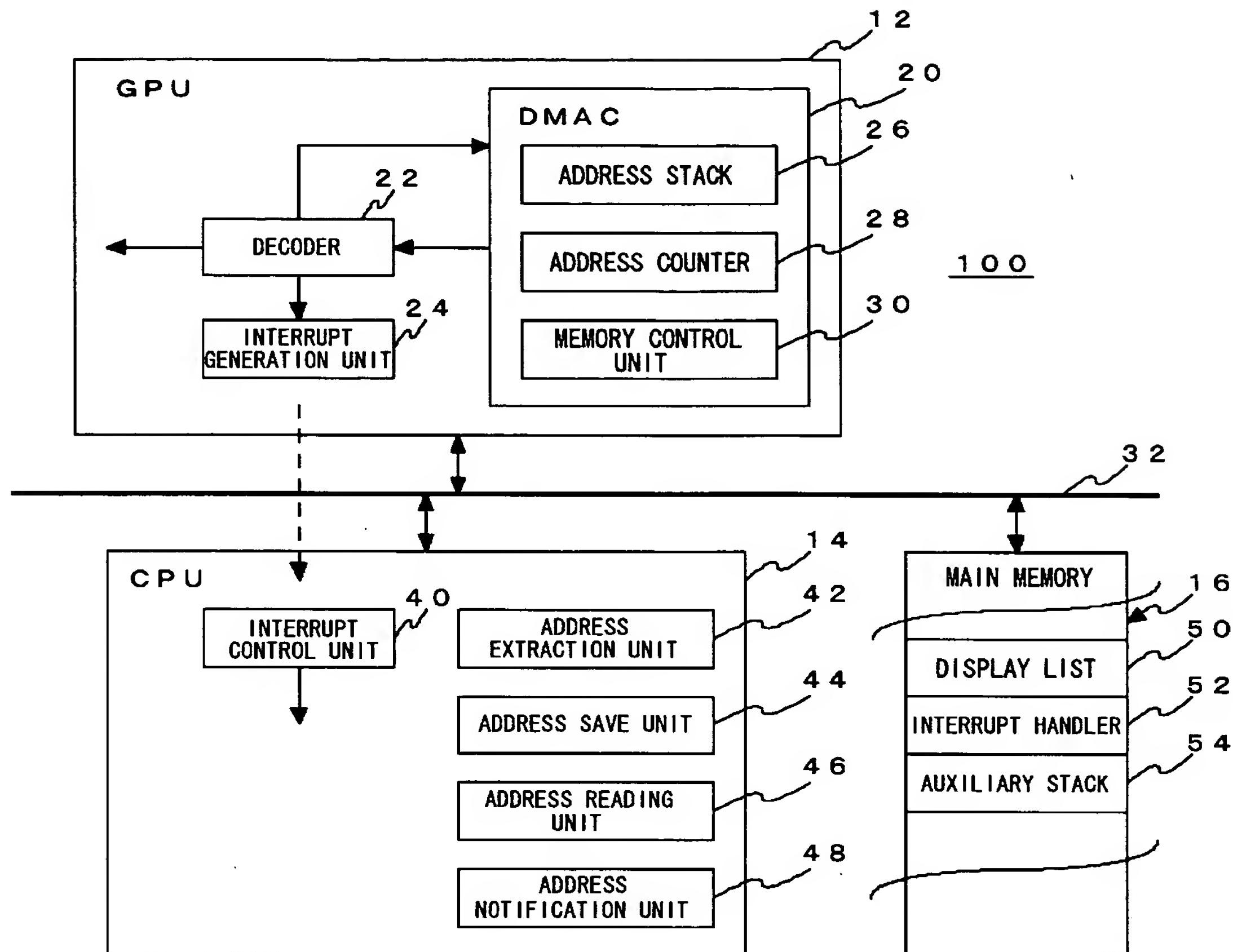
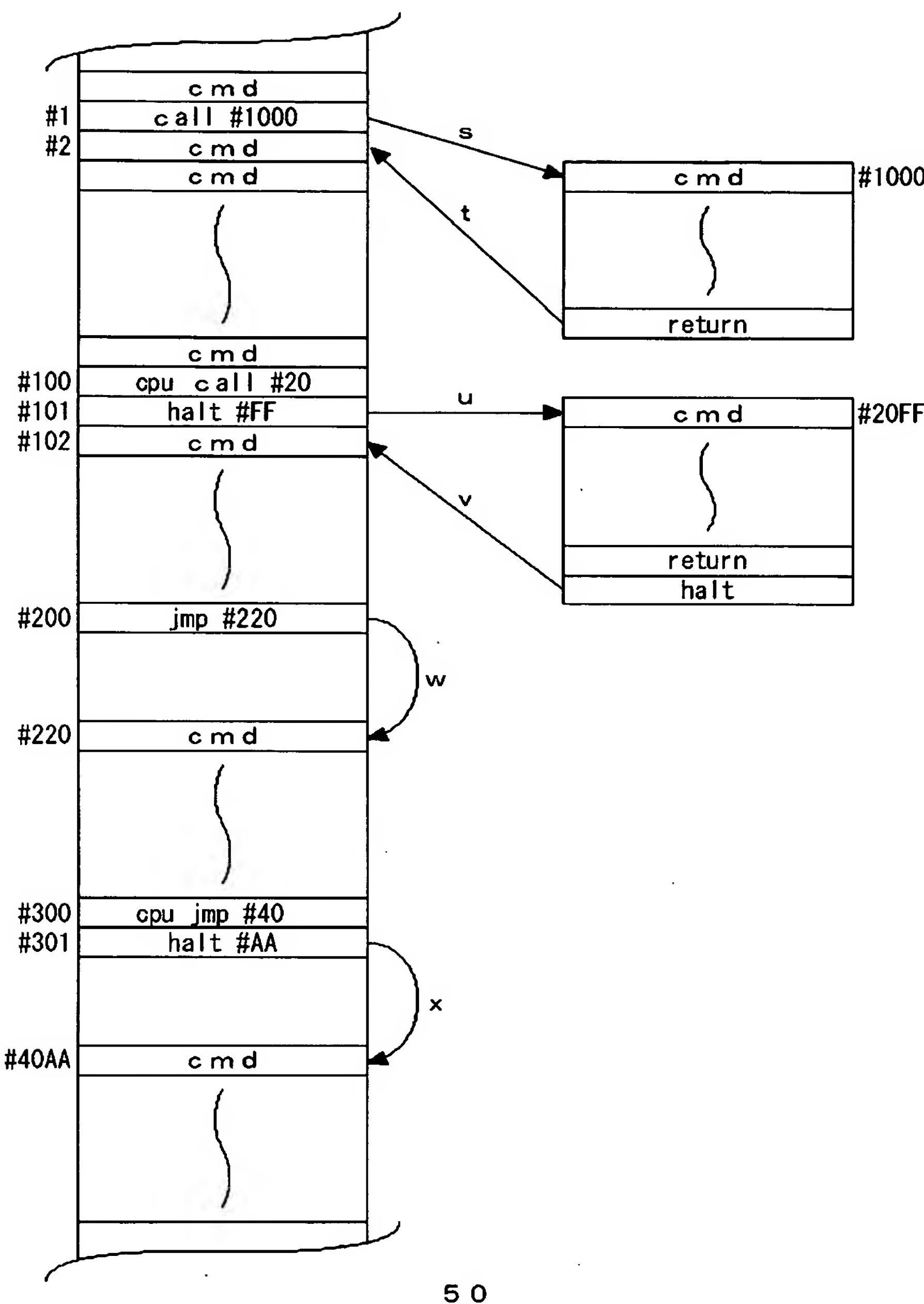


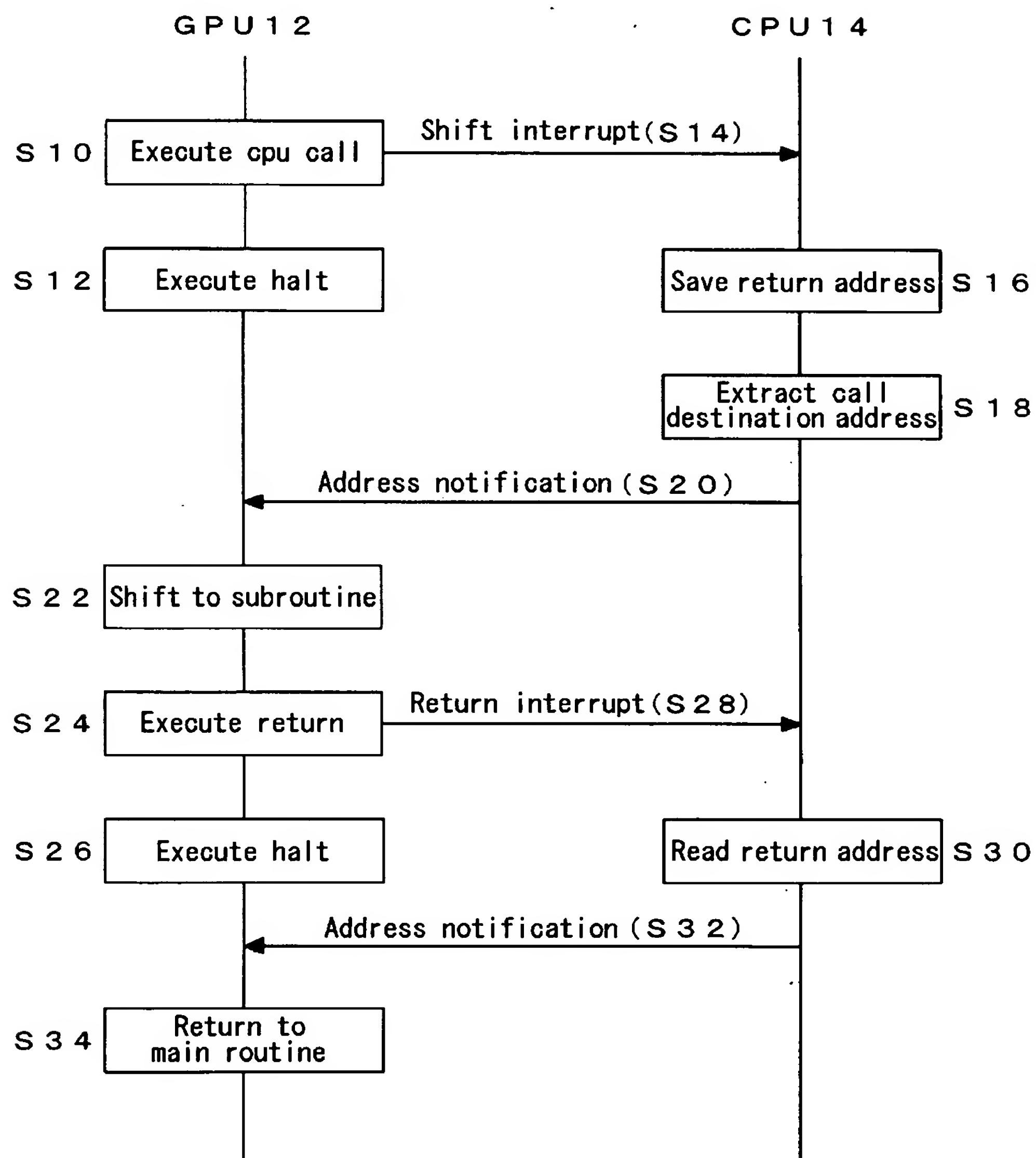
**FIG. 1**

**FIG. 2****FIG. 3**

	31	23	15	0
cpu call	E 1	O 1	addr 16-31	
halt	E 2	-	addr 0-15	

**FIG. 4**

	31	23	15	0
cpu jmp	E 1	0 0	addr 16-31	
halt	E 2	-	addr 0-15	

**FIG. 5**

**FIG. 6**